

ABSTRACT

An apparatus such as a television signal receiver includes first and second circuit boards. The first circuit board includes a memory, and control circuitry for controlling at least one function of the apparatus. The second circuit board is operably coupled to the first circuit board via control lines. The second circuit board includes a controller for generating first and second control signals. The control lines transmit the first control signals from the controller to the memory when the apparatus is in a first operational state, and transmit the second control signals from the controller to the control circuitry when the apparatus is in a second operational state. To prevent inadvertent writes to the memory during the second operational state, the controller places the memory in an unpowered state when the controller transmits the second control signals to the control circuitry. Also, means are provided to prevent the memory from keeping the control lines in one state, for example, low level, during the unpowered state to allow communications to continue on the control lines.